

# Safety-critical systems design: the TASTE tool-chain



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# High-integrity software constraints

- Real-Time determinism



- Safety & security



- Memory & processing constraints



# Usual development process: myth

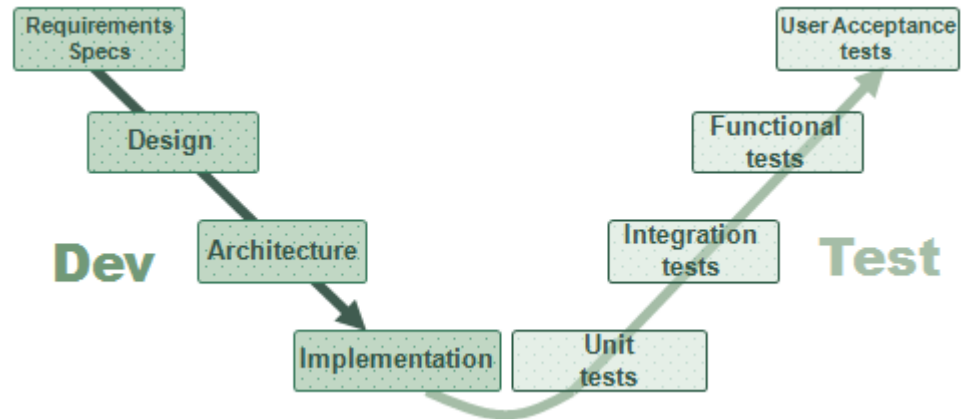
1. Specifications by designers

2. Validation by engineer

3. Development by voodoo coders

4. Tests, verification by engineers

5. Release by business consultants/sales dept.



# Usual development process, overview

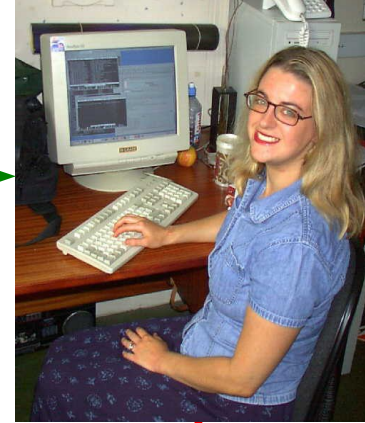
Design



Specifications



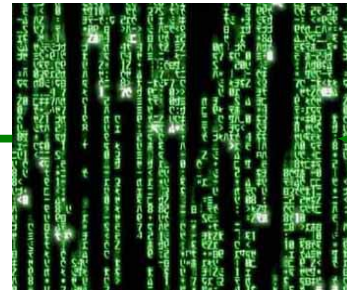
Validation



Validated specifications



Verification, qualification

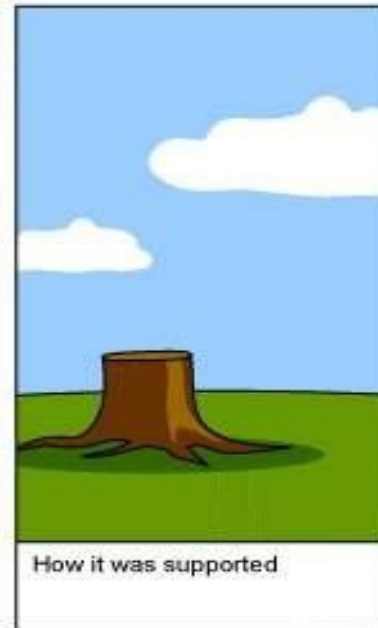
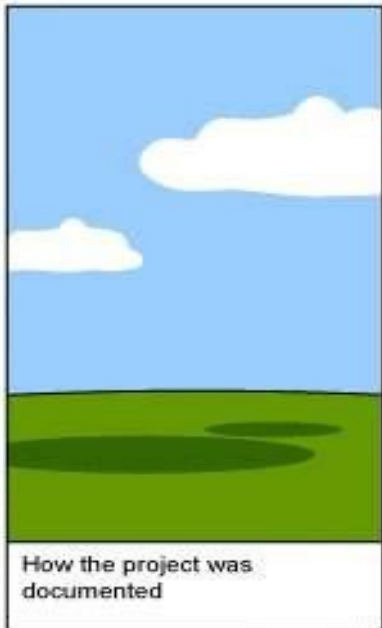


Program (binary)



Implementation

# Usual development process, reality (1)



# Usual development process, reality (2)

	DEVELOPERS	DESIGNERS	PROJECT MANAGERS	QA	SysAdmins
SEEN BY DEVELOPERS					
SEEN BY DESIGNERS					
SEEN BY PROJECT MANAGERS					
SEEN BY QA					
Seen By SysAdmins					

# Funny but ...

- Nor for life-/mission- critical systems
- Must do the *dirty* and *boring* work
- And do it *correctly*



# In addition ...

- **Requirements and constraints increase**
  - Number of functions and their impacts
  - Costs (money, time)
- **Allocated resources decrease**
  - Budget
  - Time, release to market
- **Cannot use traditional methods**



# Key points

- **Validation**

- **Automation**

- **Verification**

# Ideal development process

1. Specifications by designers

2. Validation by **engineer** analysis tools

3. Development by ~~vooodoo-coders~~ code generators

4. Tests/verif by ~~engineers~~ execution analysis tools

5. Release by business consultants/sales dept.



# TASTE guidelines

- **Abstract** software & hardware
- Focus on **engineering concerns**
- **Validate & verify** as early as possible
- **Automate** as much as possible

# TASTE process

1. Define **system interfaces**

2. Abstract **soft & hard aspects**

3. **Validate & verify** requirements

4. **Generate application using ACG**

# TASTE development process

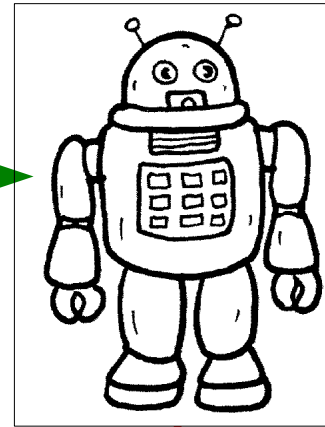
Design



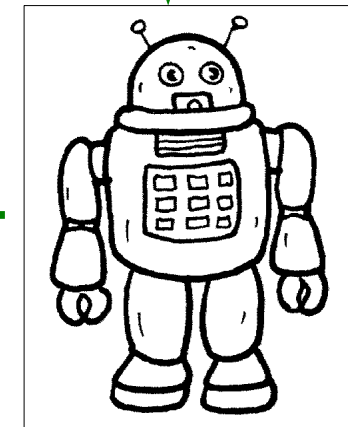
Specifications



Validation



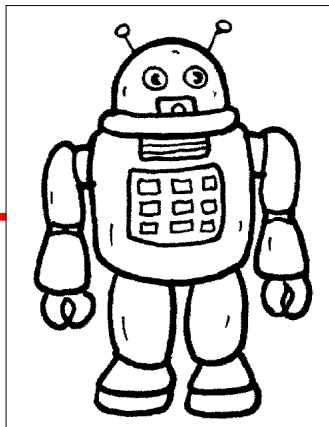
Validated specifications



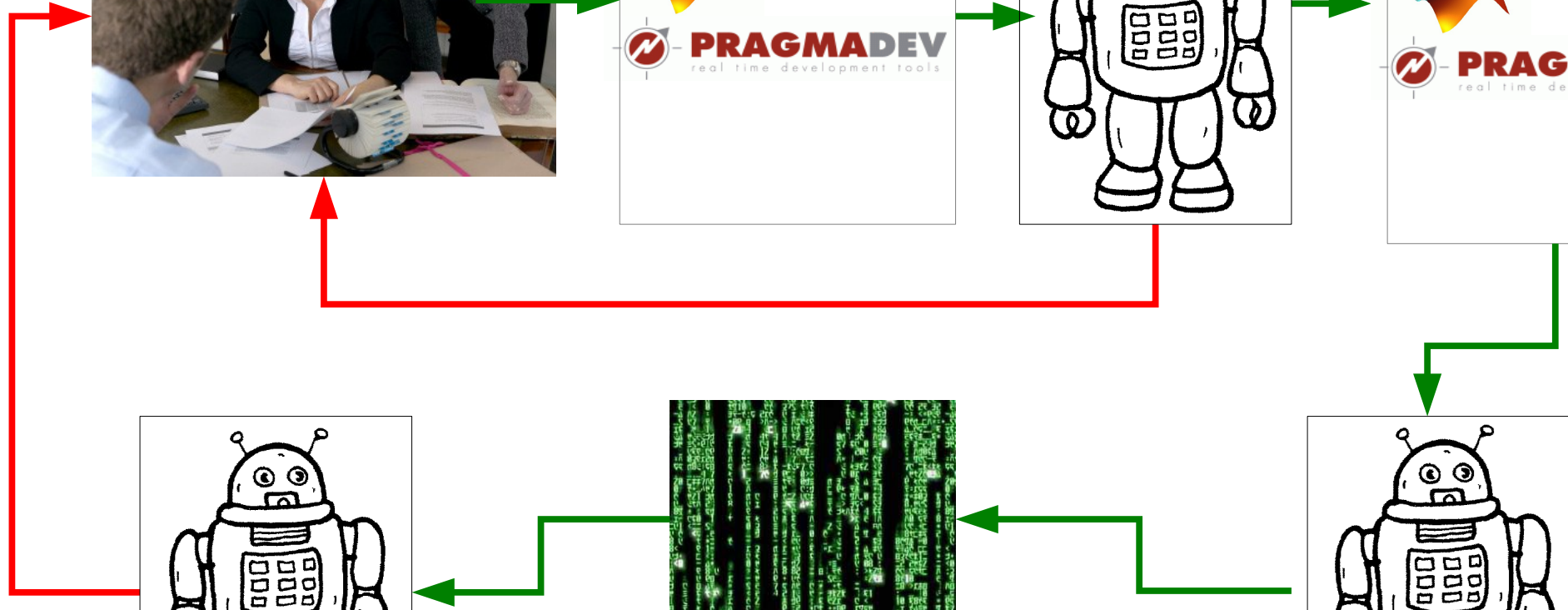
Implementation



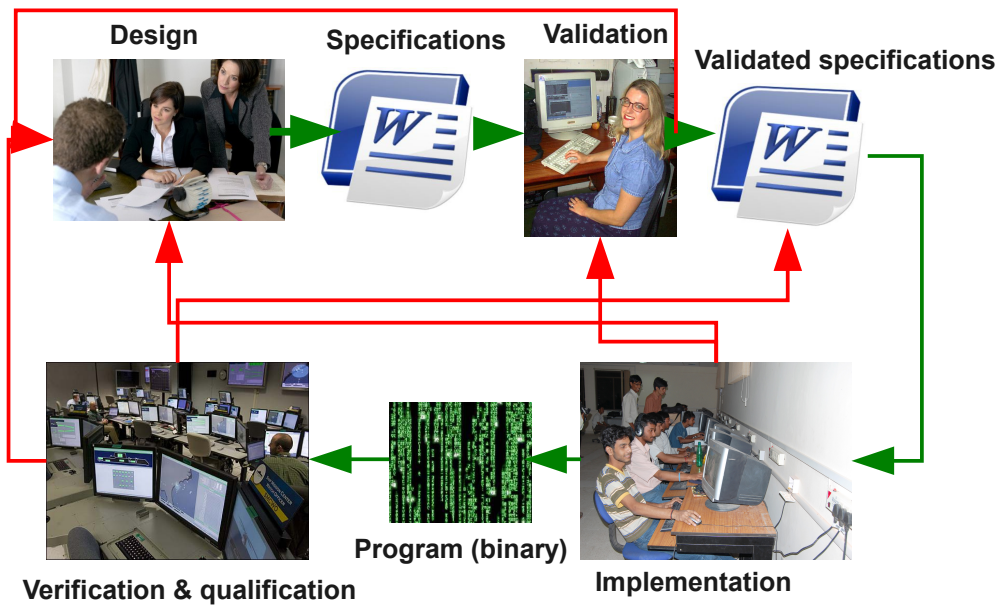
Program (binary)



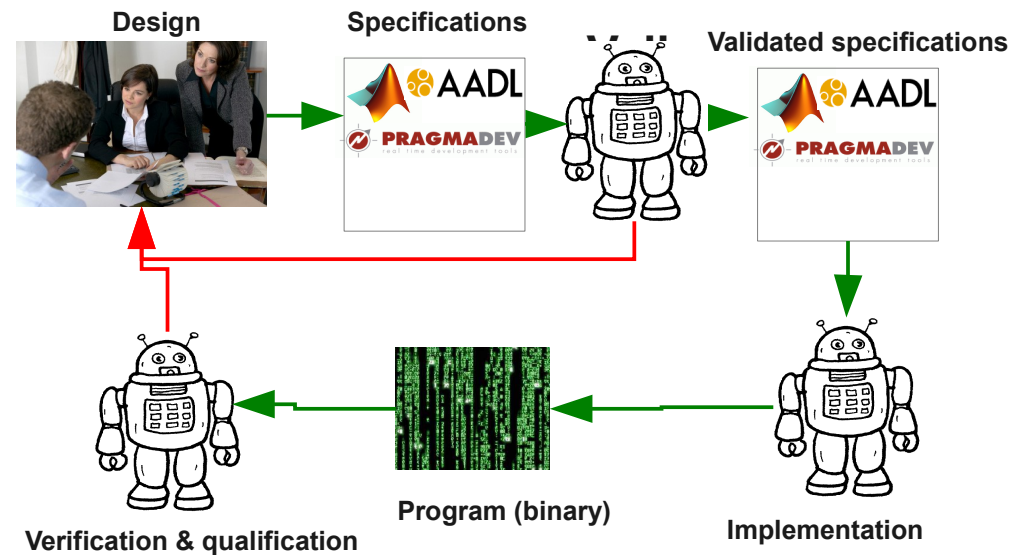
Verification, qualification



# TASTE benefits



Traditional process

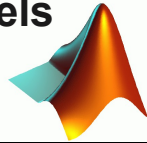


TASTE process

# TASTE workflow

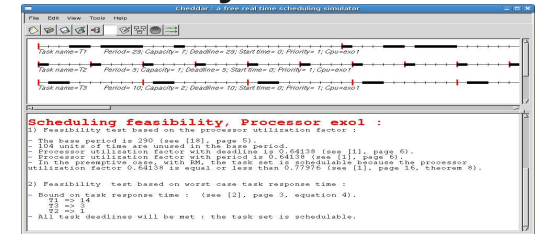
## Specifications

- Interfaces specifications
- Software models
- Deployment models



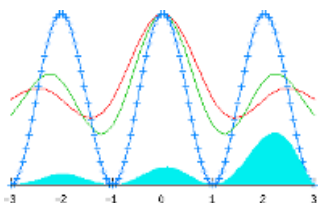
## Validation

- Scheduling
- Trade-off analysis
- ...

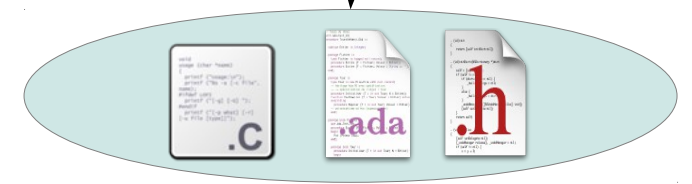


## Verification & Qualification

- System execution
- Documentation generation
- Run-time analysis
- Software metrics acquisition



## Automatic Code Generation



# TASTE technologies (1)

1. System interfaces: **ASN.1**

2. Soft specifications: **C/Ada, Simulink, SDL**




3. Hard deployment & conf: **AADL**




# TASTE technologies (2)

## Specifications




- Cheddar 
- MAST 
- Ocarina/REAL 

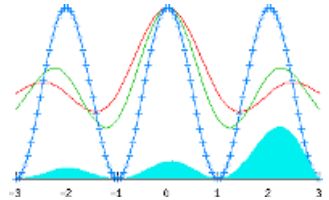


**Validation**

- ASN1 Compilers
- Ocarina
- Matlab/Simulink
- ...



**Automatic Code Generation**

- COUVERTURE
- Qemu 
- Gprof
- GNUplot

**QEMU Verification**

# TASTE use-case

## ARM movement acquisition



Data transmission through PCI

## TASTE system

- Data acquisition from devices
- Heterogeneous software (Simulink, RTDS, bare-C)



## ARM movement reproduction



Data transmission through ethernet

# Demonstration

**1. Interfaces and functions specifications**

**2. System validation**

**3. Automatic implementation**

**4. Verification**



# Conclusion

- ✓ **OSS tool-chain** for safety-critical systems
- ✓ Support by **industry & academia**
- ✓ Evaluation with **real developments**

# Perspectives

- **Enrich validation aspects**
- **Design OSS application code generators**
- **Improve verification tools**

<http://www.assert-project.net/taste>

